



Serial No. 09/751,659
Docket No.: 55419 (70158)

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE **RECEIVED**

In re Application of:

MARTIN, Brian James

Serial No.: 09/751,659

Filed: December 30, 2000

)
)
)
)
)
)

Group Art Unit: 2127

Examiner: Tang, Kenneth

OCT 18 2004

Technology Center 2100

For: **PARALLEL DISPATCH WAIT SIGNALING METHOD, METHOD FOR
REDUCING CONTENTION OF HIGHLY CONTENTED DISPATCHER
LOCK, AND RELATED OPERATING SYSTEMS, MULTIPROCESSOR
COMPUTER SYSTEMS AND PRODUCTS**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Amendment in Response to Non-Final Office Action

Sir:

In response to the Office Action dated May 10, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 17 of this paper.

Certificate of Express Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail Label No. EV 438 975 590 US class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22202 on

10/17/04

Date of Deposit

Elena Misiaszek